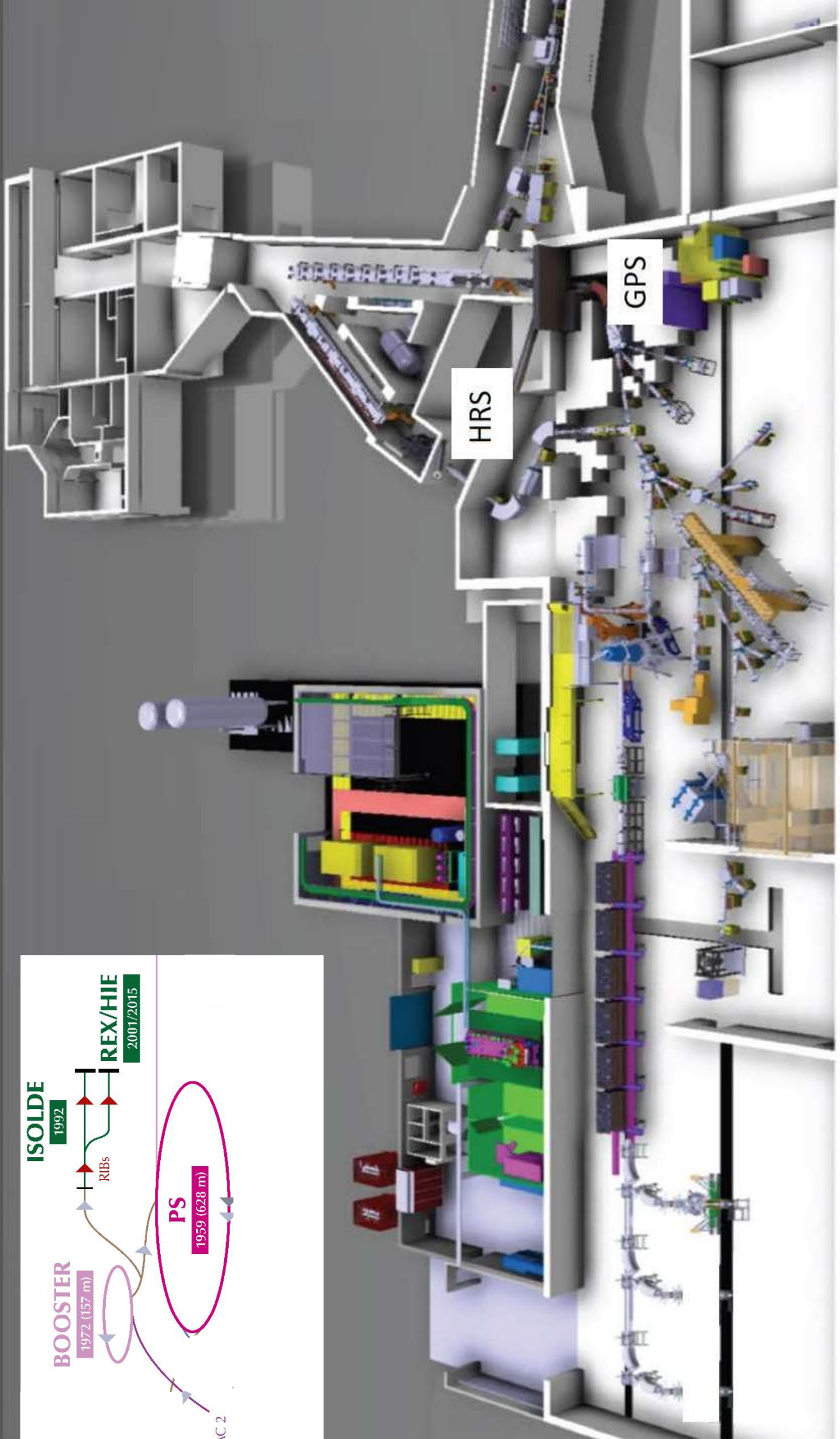
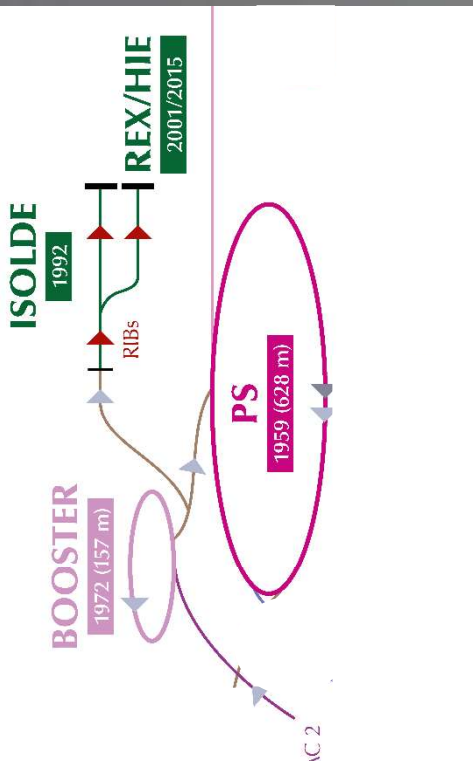
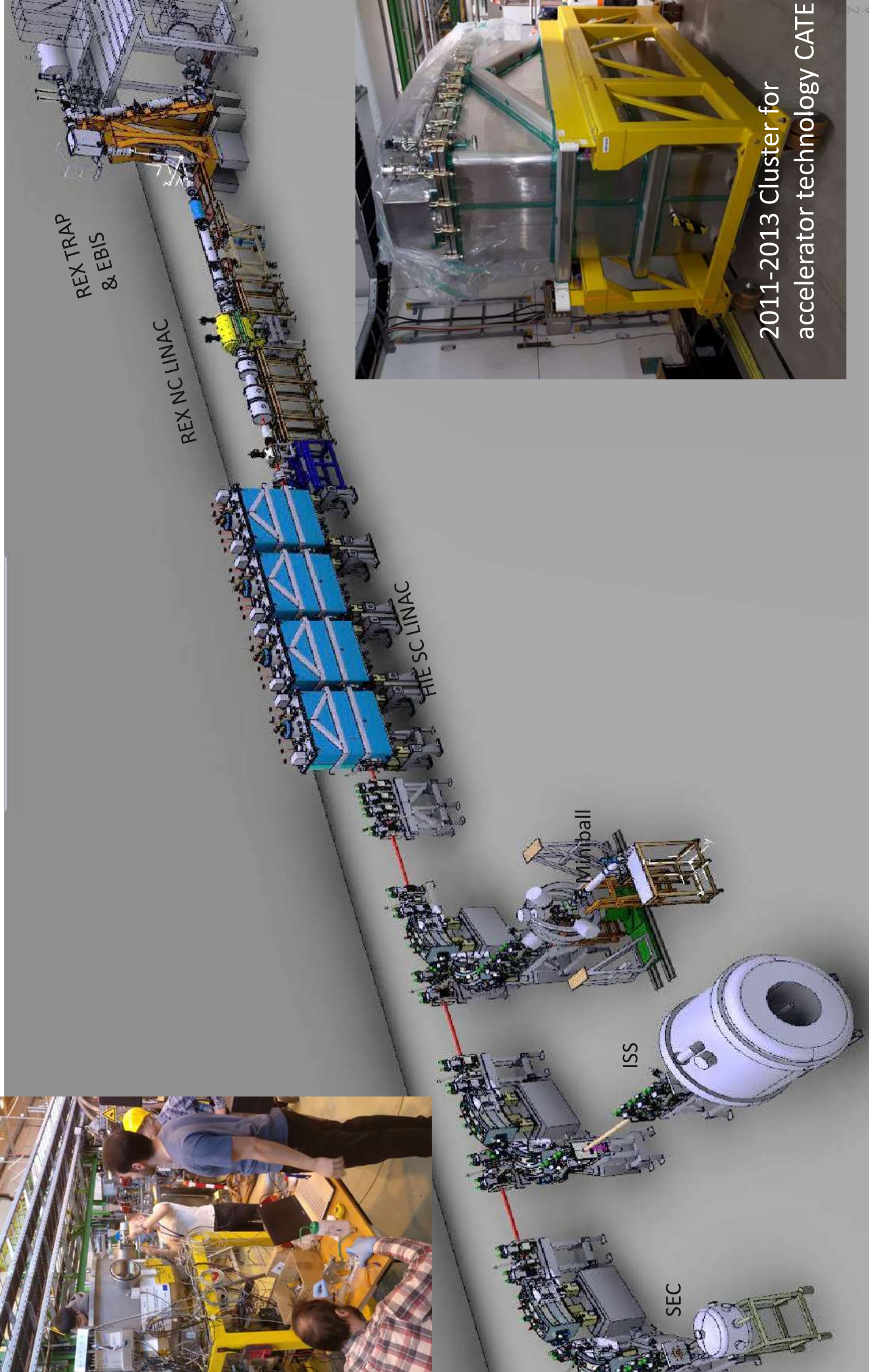


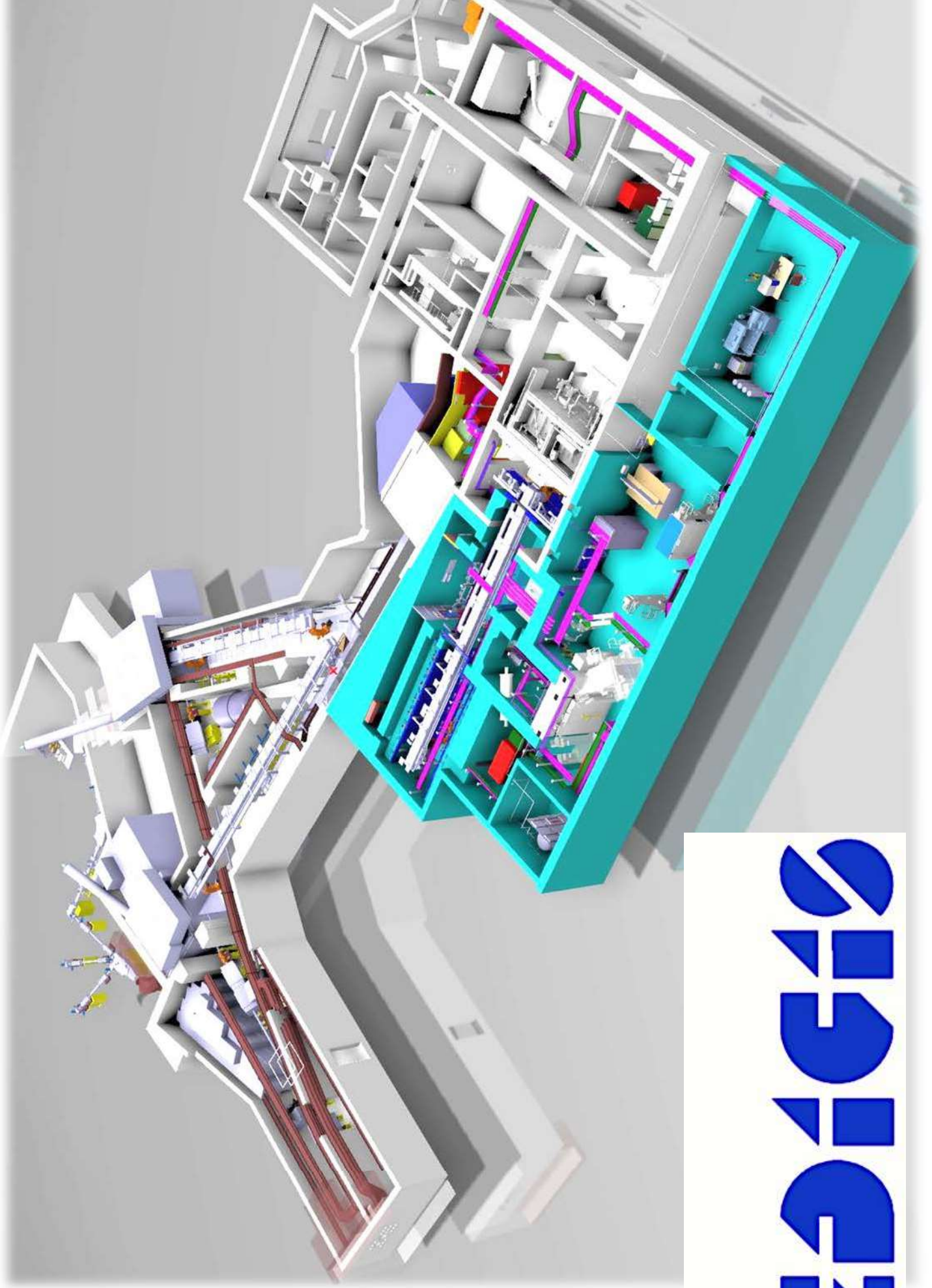
# ISOLDE bidrag til CERN-up



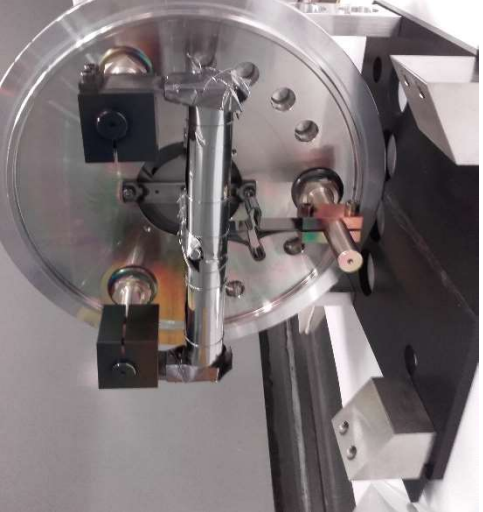




2011-2013 Cluster for  
accelerator technology CATE

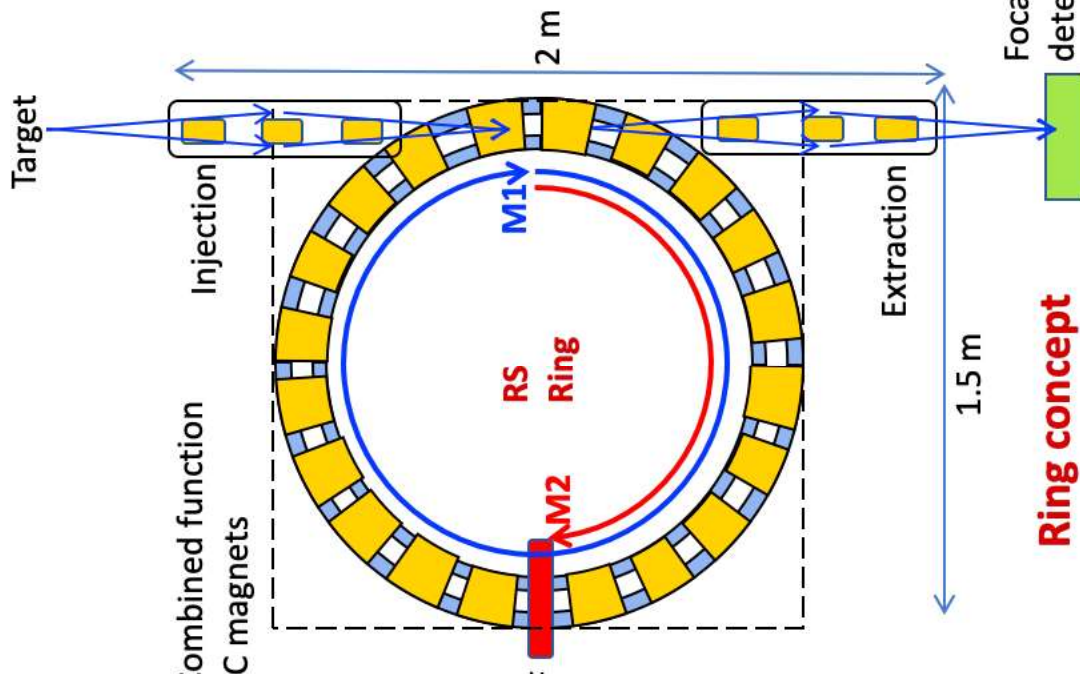


**SHIGES**

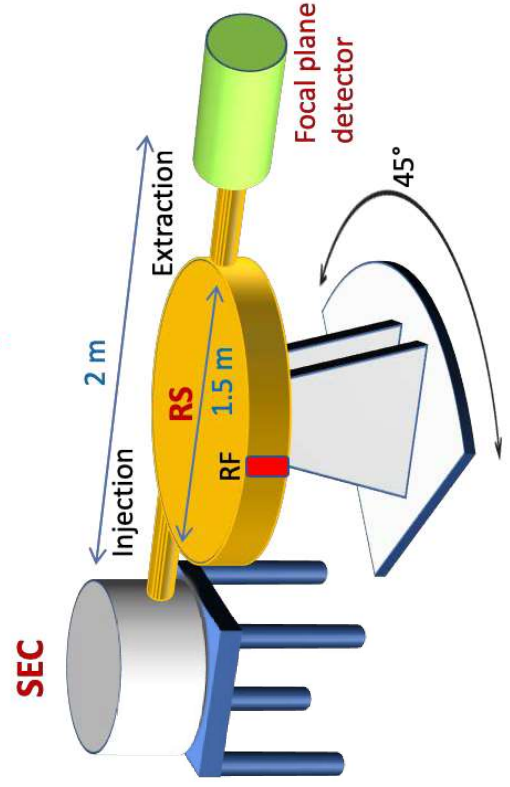


**ISO 1001**

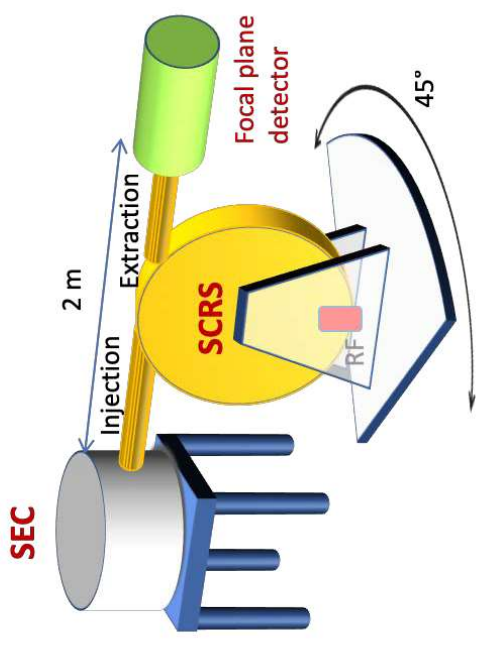
# Skizzenkonzept – Ring concept



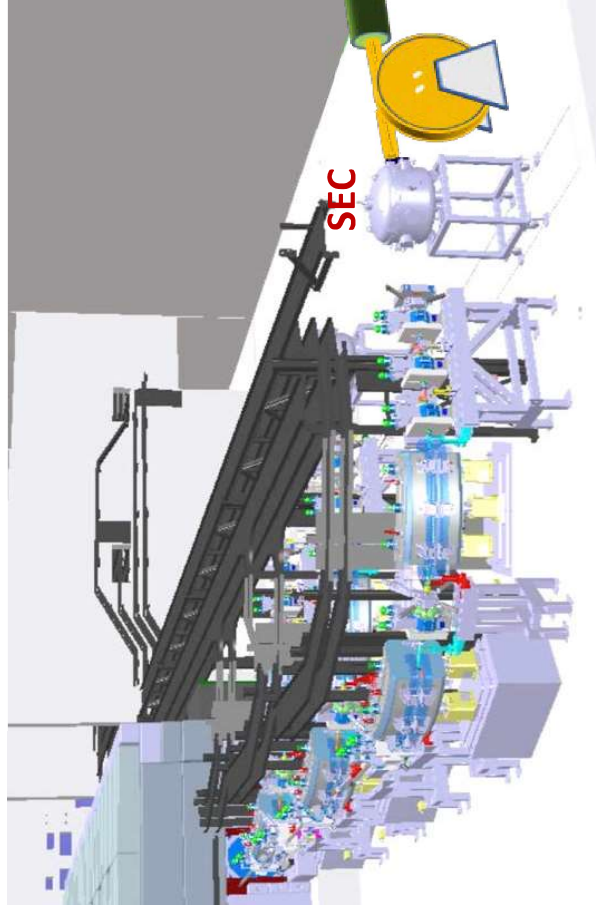
## Horizontal layout



## Vertical layout



R [m]	0.5 - 1
B [T]	2 - 6
$f_0$ [MHz]	10 - 40
$f_k$ [kHz]	100 - 3000
Storage t [ $\mu$ s]	0.5 - 10



## Ring concept